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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT. CLMS	IND CLMS
10/722,814	11/26/2003	1765	770	29936/39765	2	8	1

CONFIRMATION NO. 9355

04743
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RECEIVED

MAR 05 2004

MARSHALL GERSTEIN

FILING RECEIPT



OC000000011987708

Date Mailed: 03/01/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Cha Deok Dong, Ichon-Shi, KOREA, REPUBLIC OF;
 Ho Min Son, Ichon-shi, KOREA, REPUBLIC OF;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2003-40155 06/20/2003

If Required, Foreign Filing License Granted: 02/26/2004

Projected Publication Date: 12/23/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of forming gate electrode in semiconductor device

Semiconductor

Preliminary Class

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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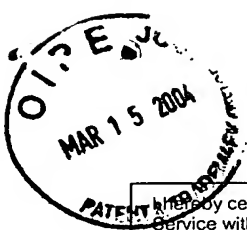
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Dated: March 11, 2004

Signature: _____

(Michael R. Hull)

Docket No.: 29936/39765
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Cha Deok Dong and Ho Min Son

Art Unit: 1765

Application No.: 10/722,814

Examiner: Not Yet Assigned

Filed: November 26, 2003

For: METHOD OF FORMING GATE ELECTRODE
IN SEMICONDUCTOR DEVICE

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in

Title:

Please delete "semiconductor" and replace it with --semiconductor-- as shown in red.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Application No.: 10/722,814

Docket No.: 29936/39765

Dated: March 11, 2004

Respectfully submitted,

By


Michael R. Hull

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